HUAWEI TECHNOLOGIES CO., LTD.

RECIPIENT

SPECIFICATIONS

MODEL : **SG-210STF**

SPEC. No. : **A13-092-0B**

DATE: **May. 8. 2013**

SEIKO EPSON CORPORATION

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SPECIFICATIONS

1. Application

(1) This document is applicable to the crystal oscillator that are delivered to HUAWEI TECHNOLOGIES CO., LTD. from Seiko Epson Corp.

(2) This product is compliant with RoHS Directive.

(3) This Product supplied (and any technical information furnished, if any) by Seiko Epson Corporation shall not be used for the development and manufacture of weapon of mass destruction or for other military purposes. Making available such products and technology to any third party who may use such products or technologies for the said purposes are also prohibited.

(4) This product listed here is designed as components or parts for electronics equipment in general consumer use. We do not expect that any of these products would be incorporated or otherwise used as a component or part for the equipment, which requires an extra high reliability, such as satellite, rocket and other space systems, and medical equipment, the functional purpose of which is to keep life.

2. Model

The model is SG-210STF.

3. Packing

It is subject to the packing standard of Seiko Epson Corp.

4. Warranty

Defective parts which are originated by us are replaced free of charge in case defects are found within 12 months after delivery.

5. Amendment and abolishment

Amendment and/or abolishment of this specification are subject to the agreement between both parties.

6. Contents

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[1] Absolute maximum ratings

Parameter	Symbol	Value	Unit	Note
Supply voltage	V_{CC}-GND	-0.3 to +4.0	V	
Storage temperature *	T_stg	-55 to +125	°C	Stored as bare product after unpacking.
Input voltage	V_{IN}	-0.3 to V_{CC}+0.3	V	$\overline{ST}$ Terminal

* Concerning the frequency change, please refer [8] Environmental and mechanical characteristics.

[2] Operating range

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Supply voltage	V_{CC}	2.2	2.5	2.7	V	
Supply voltage	GND	0.0	0.0	0.0	V	
Input voltage	V_{IN}	GND	-	V_{CC}	V	
Operating temperature	T_use	-40	+25	+105	°C	
Output load condition	L_CMOS	-	-	15	pF	

- Start up time(0 %V_{CC}→90 %V_{CC}) of power source should be more than 150 μs.
- By-pass capacitor (0.01 μF to 0.1 μF) is connected near Vcc between Vcc and GND.
  (Refer to [11] Recommendable patterning)

[3] Frequency characteristics

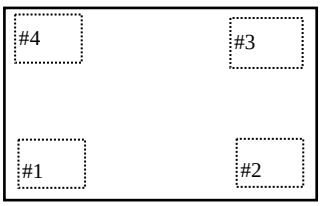
Output frequency (f_0) ..... 25 MHz

Parameter	Symbol	Value[1×10^{-6}]	Note
Frequency tolerance *	f_tol (OSC)	Y: ± 50	T_use=-40 °C to +105 °C
Aging	F_aging	± 3	T_use=+25 °C, V_{CC}=3.3 V First year

* This includes initial frequency tolerance, temperature characteristics, input voltage characteristics, and load characteristics, but excludes aging.

[4] Terminal assignment

Top View



Terminal name	Terminal No.	Terminal type.
$\overline{ST}$	1	INPUT
GND	2	—
OUT	3	OUTPUT
V_{CC}	4	—

$\overline{ST}$ pin : High or open. → Specified frequency output = enable.

$\overline{ST}$ pin : Low. → Output is high impedance = disabled.

* When the $\overline{ST}$ terminal is not controlled, it should be connected to the Vcc terminal.

[5] Electrical characteristics

(Please see page 2 [2] Operating range)

Parameter	Symbol	Value		Unit	Note
		Min.	Max.		
Start up time	t_{OSC}	-	3	ms	t=0 at 90 %V_{CC}
Current consumption	I_{CC}	-	4.0	mA	No load
Standby current	I_std	-	4.0	μA	$\overline{ST}$ =GND Ta<+105℃
Output rise time	tr	-	3.0	ns	20 %V_{CC} → 80 %V_{CC}
Output fall time	tf	-	3.0	ns	80 %V_{CC} → 20 %V_{CC}
Symmetry	SYM	45	55	%	50 %V_{CC} Level
High level output voltage	V_{OH}	0.9 V_{CC}	-	V	I_{OH} = -1 mA
Low level output voltage	V_{OL}	-	0.1 V_{CC}	V	I_{OL} = 1 mA
High level input voltage	V_{IH}	0.8 V_{CC}	-	V	$\overline{ST}$ terminal
Low level input voltage	V_{IL}	-	0.2 V_{CC}	V	$\overline{ST}$ terminal
Input current	I_{IH}	-	10	μA	V_{IN} = V_{CC}
	I_{IL}	-10	-	μA	V_{IN} = GND
Output disable time	tstp	-	100	ns	$\overline{ST}$ terminal High → Low
Output enable time	tsta	-	3	ms	$\overline{ST}$ terminal Low → High

Refer to [6] Test circuit [7] Timing chart

・Jitter Characteristic

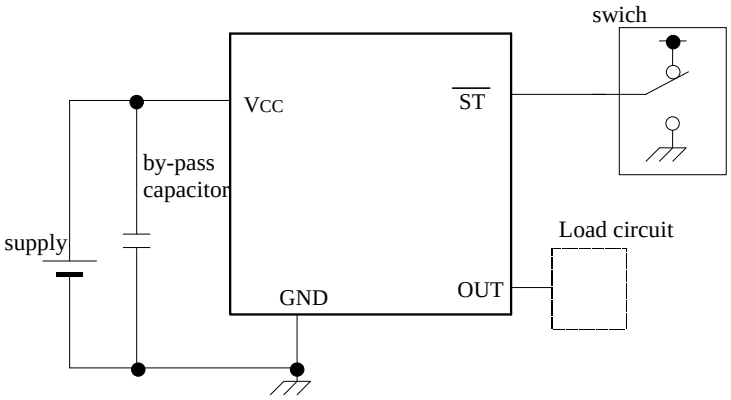
(Please see page 2 [2] Operating range)

Parameter	Symbol	Value		Unit	Note
		Typ.	Max.		
Phase Jitter	t_{PJ}	-	1	ps	Offset Frequency:5kHz～20MHz
Peak to Peak Jitter	tp-p	-	35	ps-pk-pk	
Phase Noise	L(s)	-145	-	dBc/Hz	1 kHz
		-152	-		10 kHz
		-158	-		100 kHz
		-160	-		1 MHz

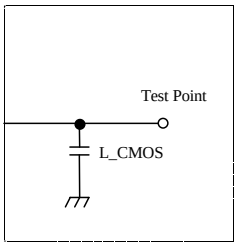
※ 1 WAVECREST Digital timing system SIA-3100C.

[6] Test circuit

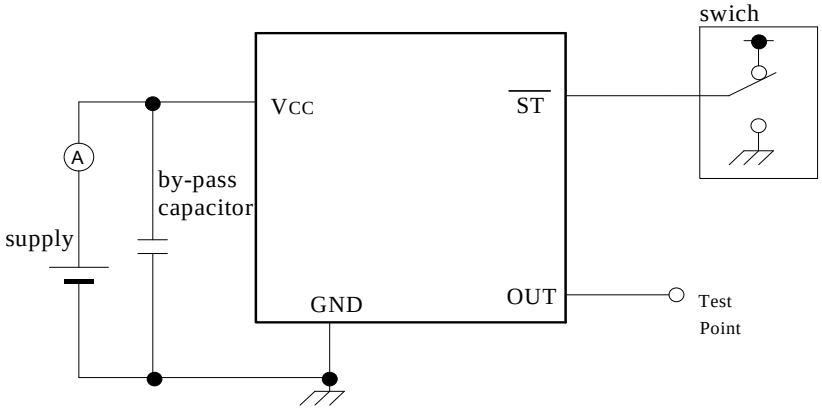
1) Waveform observation



Load circuit load capacitance



2) Current consumption



*Current consumption under the disable function should be $\overline{ST}$ = GND.

3) Condition

(1) Oscilloscope

- Band width should be minimum 5 times higher (wider) than measurement frequency.
- Probe earth should be placed closely from test point and lead length should be as short as possible.

* Recommendable to use miniature socket. (Don't use earth lead.)

(2) L_CMOS also includes probe capacitance.

(3) By-pass capacitor (0.01 μF to 0.1 μF) is placed closely between V_{CC} and GND.

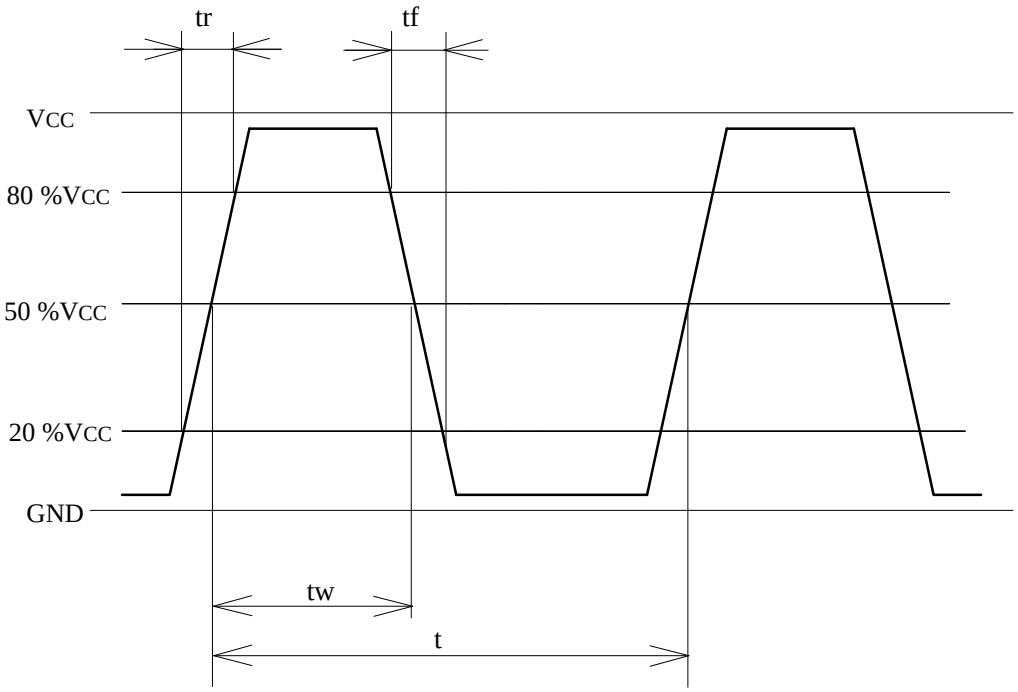
(4) Use the current meter whose internal impedance value is small.

(5) Power supply

- Start up time (0 %V_{CC} → 90 %V_{CC}) of power source should be more than 150 μs.
- Impedance of power supply should be as lowest as possible.

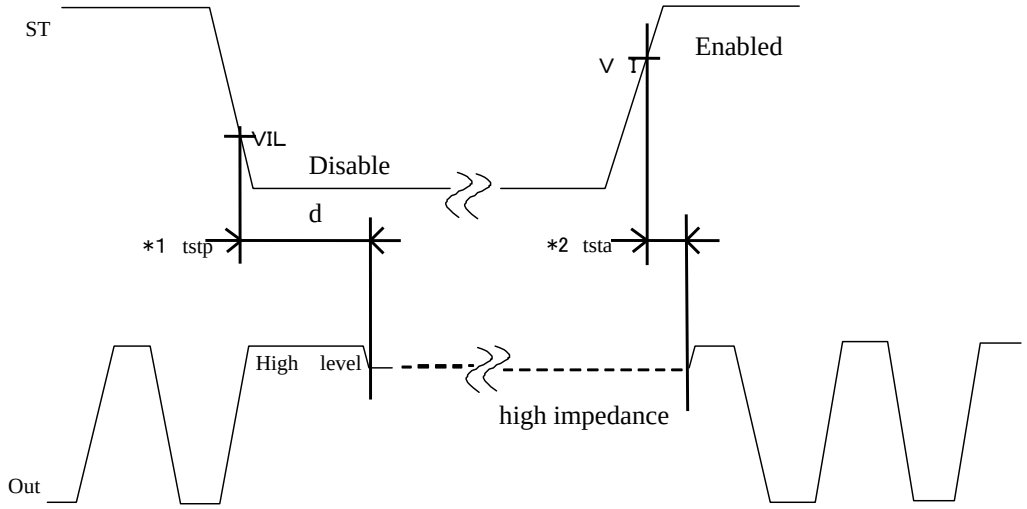
[7] Timing chart

1) C-MOS load SYM = tw/t × 100 (%)



2) $\overline{ST}$ function and timing

$\overline{ST}$ function	Osc. circuit	Output status
High or Open	Oscillation	Specified frequency is output : Enable
Low	Oscillation stop	Output becomes high impedance : Disable



*1 The time taken from $\overline{ST}$ =VIL to output = Disable (high impedance)

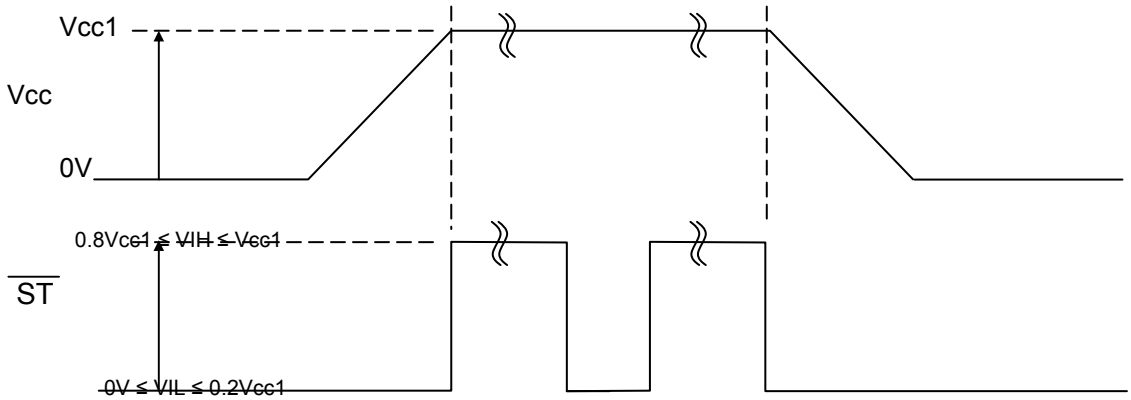
*2 The time taken from $\overline{ST}$ =VIH to output = Start

Output start : VOH≥80%VCC, VOL≤20 %VCC, fout = fo±1 000×10^{-6}

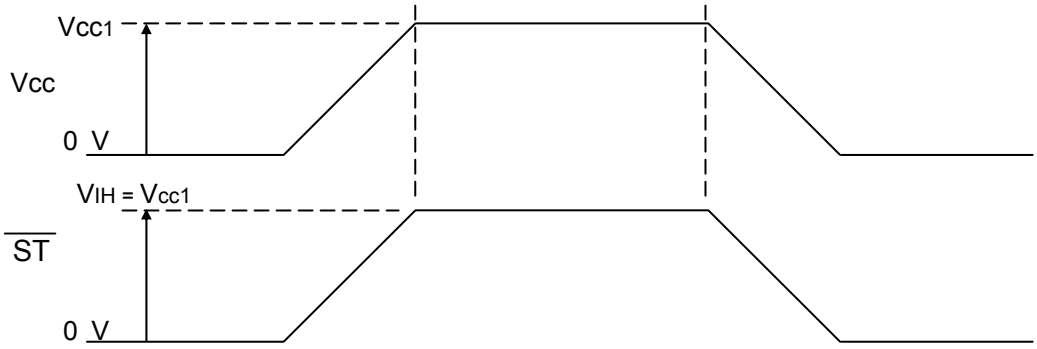
3) $\overline{ST}$ Control timing

$\overline{ST}$ function is used on the voltage below supply voltage.

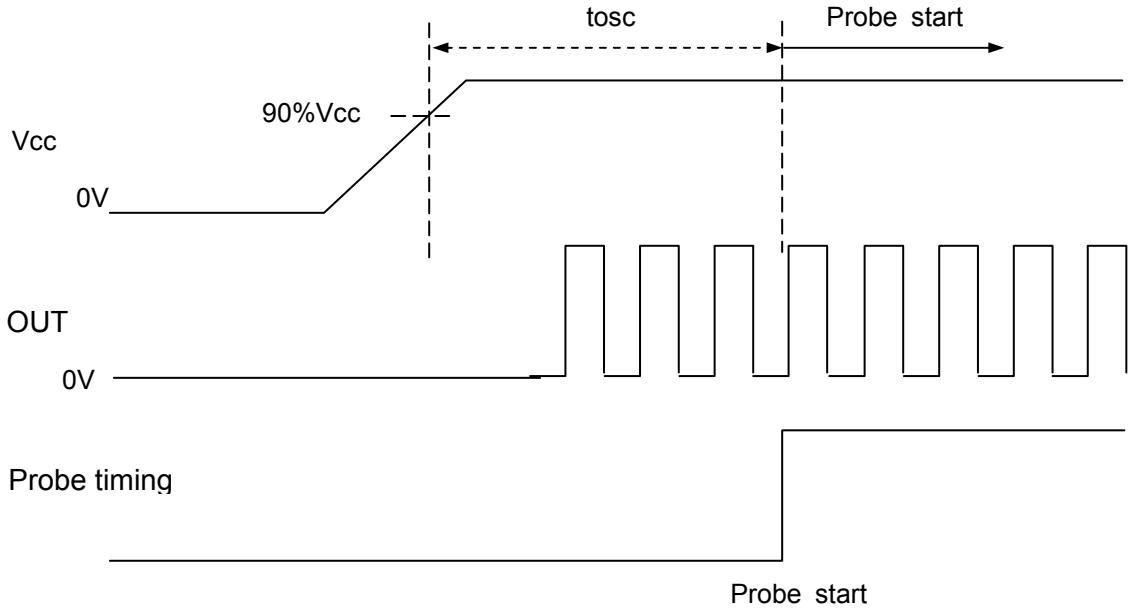
$\overline{ST}$ control timing differs from Vcc control timing



$\overline{ST}$ terminal is connected to Vcc terminal



4) Timing of an output frequency signal



[8] Environmental and mechanical characteristics

(The company evaluation condition We evaluate it by the following examination item and examination condition.)

No.	Item	Value *1		Test Conditions
		Δ f / f *2 [1×10^{-6}]	Electrical characteristics	
1	High temperature storage	*3 ± 20	Satisfy Item [5] after test.	+125 °C × 1 000 h
2	Low temperature storage	*3 ± 10		-40 °C × 1 000 h
3	High temperature bias	*3 ± 10		+105 °C × V_{CC} Max. × 1 000 h
4	Low temperature bias	*3 ± 10		-40 °C × V_{CC} Max. × 1 000 h
5	Temperature humidity bias	*3 ± 10		+85 °C × 85 %RH × V_{CC} Max. × 1 000 h
6	Temperature cycle	*3 ± 10		-40 °C ↔ +125 °C 30 min. at each temperature 100 cycles
7	Resistance to soldering heat	± 3		Convection reflow soldering furnace (3 time) Ref. IPC/JEDEC J-STD-020D.1
8	Shock	± 3		150 g dummy Jig (EPSON TOYOCOM Standard) drop from 1 500 mm height on the Concrete 3 directions 10 times.
9	Vibration	± 2		10 Hz to 55 Hz amplitude 0.75 mm 55 Hz to 500 Hz acceleration 98 m/s^2 10 Hz → 500 Hz → 10 Hz 15 min./cycle 6 h (2 hours , 3 directions)
10	Seal	1×10^{-9} Pa·m^3/ s		He leakage detector
11	Solderability	Termination must be 95 % covered with fresh solder		Dip termination into solder bath at +235 °C ± 5 °C for 5 s. (Using Rosin Flux)
12	Pull - off	No peeling-off at a solder part		10 N press for 10 s ± 1 s Ref. EIAJ ED-4702

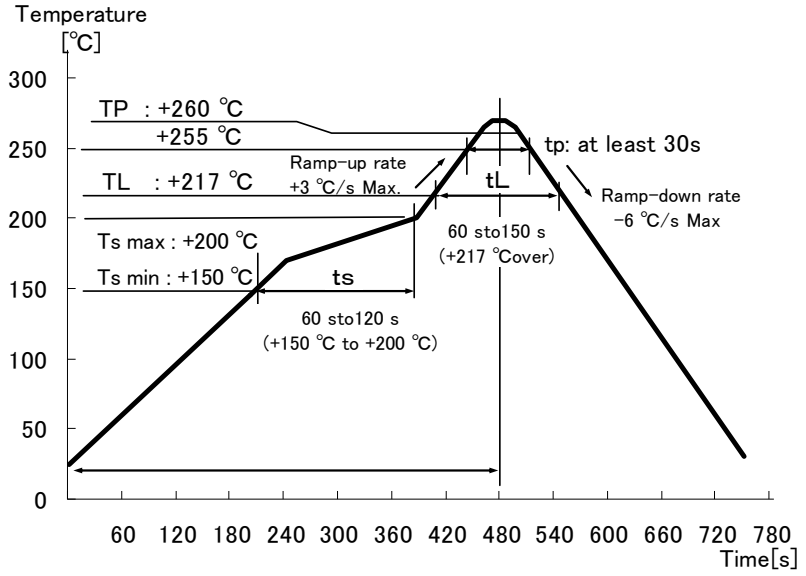
< Notes >

*1 Each test done independently.

*2 Measuring 2 h to 24 h later leaving in room temperature after each test.

*3 Initial value shall be measured after 24 h storage at room temperature after pre-conditioning.
Pre-conditioning: Reflow (3 time)

Convection reflow condition （IPC/JEDEC J-STD-020D.1）



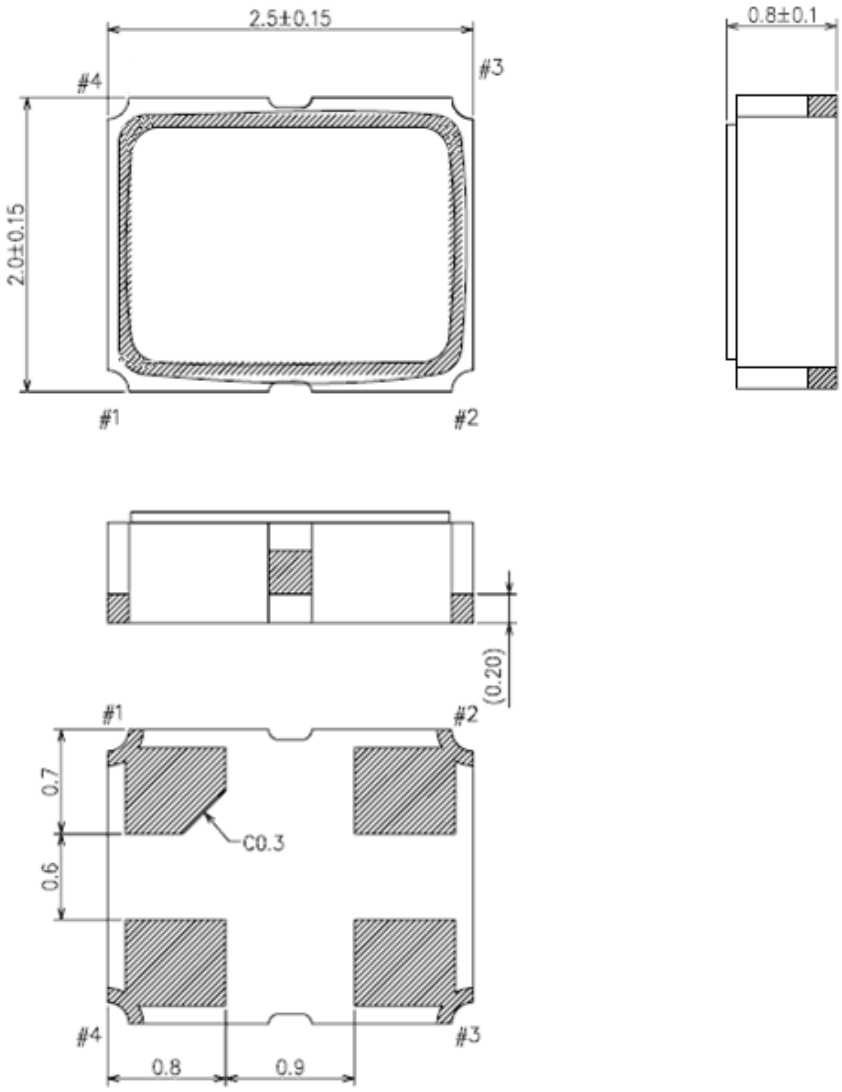
[9] Electro Static Discharge

・ESD

Item	Electro Static Discharge	Test term
HBM	2 000 V Min.	EIAJ ED-4701-1 C111A,100pF,1.5KΩ, 3 time
MM	200 V Min.	EIAJ ED-4701-1 C111,200pF, 0Ω, 1 time

[10] Dimensions and marking layout

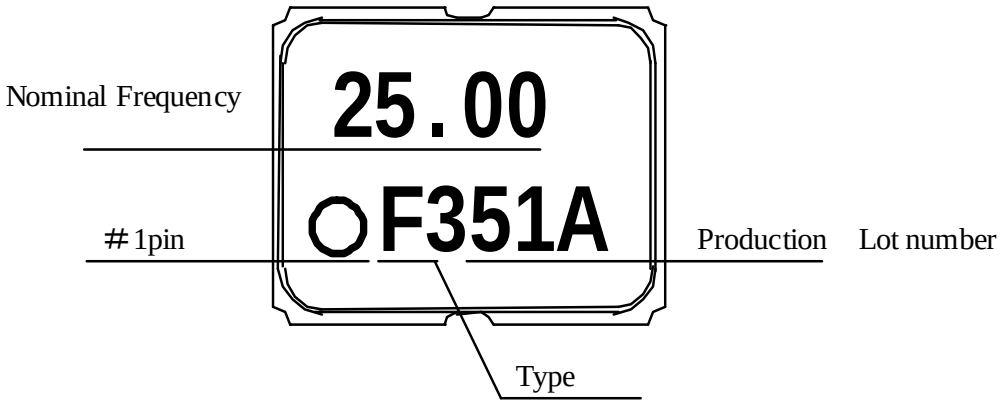
1) Dimensions



Terminal treatment : Au plating

Unit : mm

2) Marking layout



- ♦ The above marking layout shows only marking contents and their approximate position and it is not for font, size and exact position.
- ♦ Output frequency shall indicate 5 digits (include decimal point), if the value of frequency over 5 digits, the least significant digits will be omitted.

[11] Notes

1) This device is made with C-MOS IC.
Please take necessary precautions to prevent damage due to electrical static discharge.

2) SEIKO EPSON recommends a 0.01 μF to 0.1 μF capacitor must be connected near Vcc between Vcc and GND to obtain stable operation and protect against power line ripple.

3) Vcc and GND pattern shall be as large as possible so that high frequency impedance shall be small.

4) SEIKO EPSON cannot recommend to put filtering element into power line so as to reduce noise. Oscillator might be unstable oscillation because high frequency impedance of power line become higher. When use filtering element, please verify electrical construction and or element's spec.

5) SEIKO EPSON doesn't recommend to power on from intermediate electric voltage or extreme fast power on, Those powering conditions may cause no oscillation or abnormal oscillation.

6) Power ripple: 200 mV P-P max. Start up time (0 %V_{CC}→90 %V_{CC}) of power source should be more than 150 μs.

7) A long output line may cause irregular output, so try to make the output line as short as possible.

8) Other high-level signal lines may cause incorrect operation, so please do not place high level signal line close to this device.

9) This device contains a crystal resonator, so please don't expose excessive shock or vibration.
SEIKO EPSON recommends store device under normal temperature and humidity to keep the specification.

10) An automatic insertion is available, however, the internal crystal resonator might be damaged in case that too much shock or vibration is applied by machine condition.
Be sure to check your machine condition in advance.

11) Ultrasonic cleaning can be used on the SG-210SCB, however, since the oscillator might be damaged under some conditions, please exercise in advance.

12) SEIKO EPSON recommends to use and store under room temperature and normal humidity to secure frequency accuracy and prevent moisture.

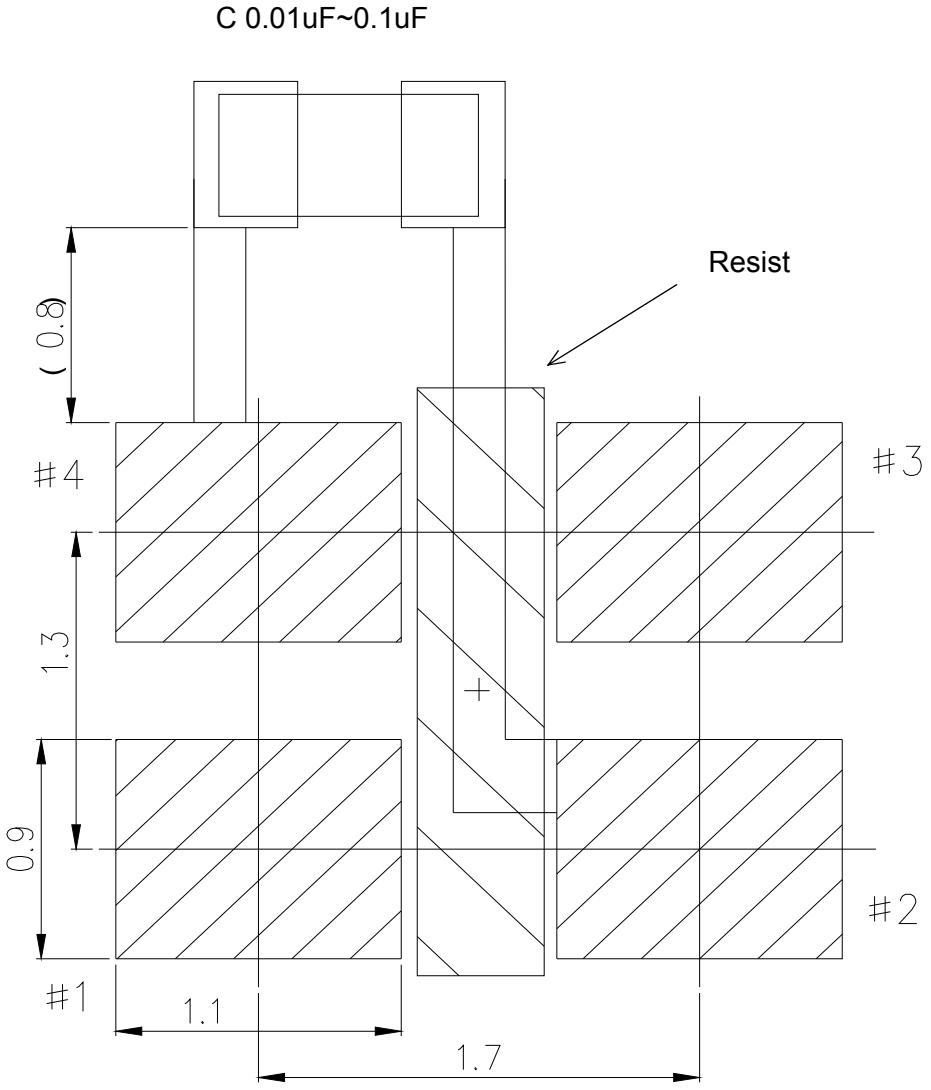
13) $\overline{ST}$ -pin has pull-up resistor internally. The resistor value is switched depending on input voltage.
Please refer to electrical characteristics.

14) Lid is electrically connected to GND.
Please don't apply electrical voltage.

[12] Recommendable patterning

The soldering pad sample indicated as like following:

Soldering position (Unit : mm)



TAPING SPECIFICATION

Ⅰ. Application

This standard will apply to 2.5×2.0 Ceramic package.

Spec：CG package

Ⅱ. Contents

Item No.	Item	Page
〔1〕	Taping specification	1 to 2
〔2〕	Inner carton	3
〔3〕	Shipping carton	
〔4〕	Marking	4
〔5〕	Quantity	
〔6〕	Storage environment	
〔7〕	Handling	

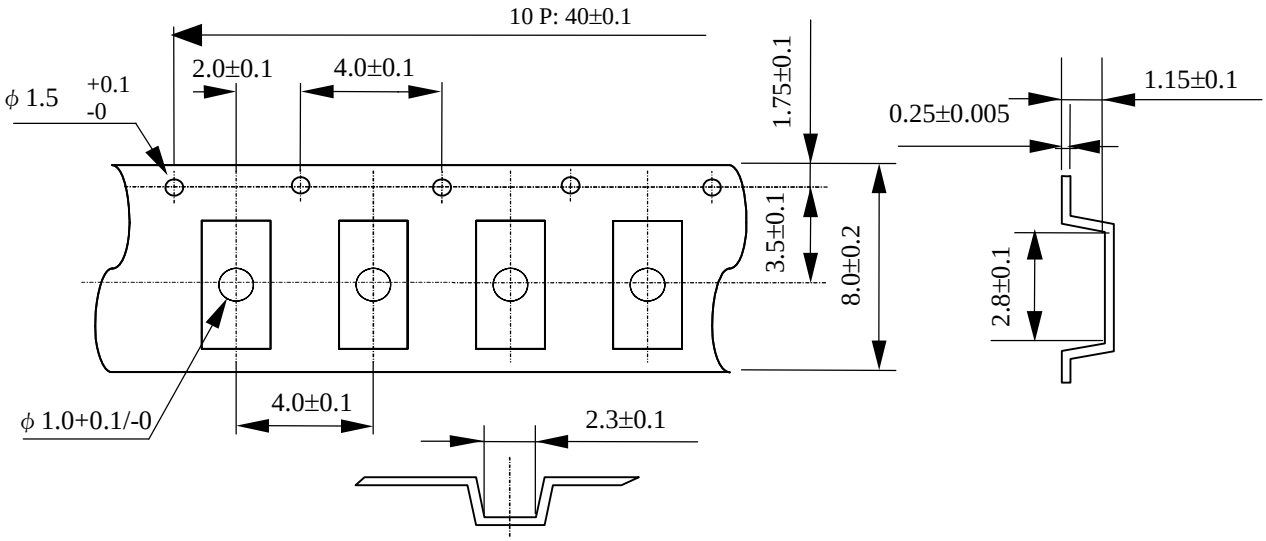
〔1〕 Taping specification

Subject to「EIA-481」and「IEC-60286」

(1) Tape dimensions

Material of the carrier tape : ＰＳ

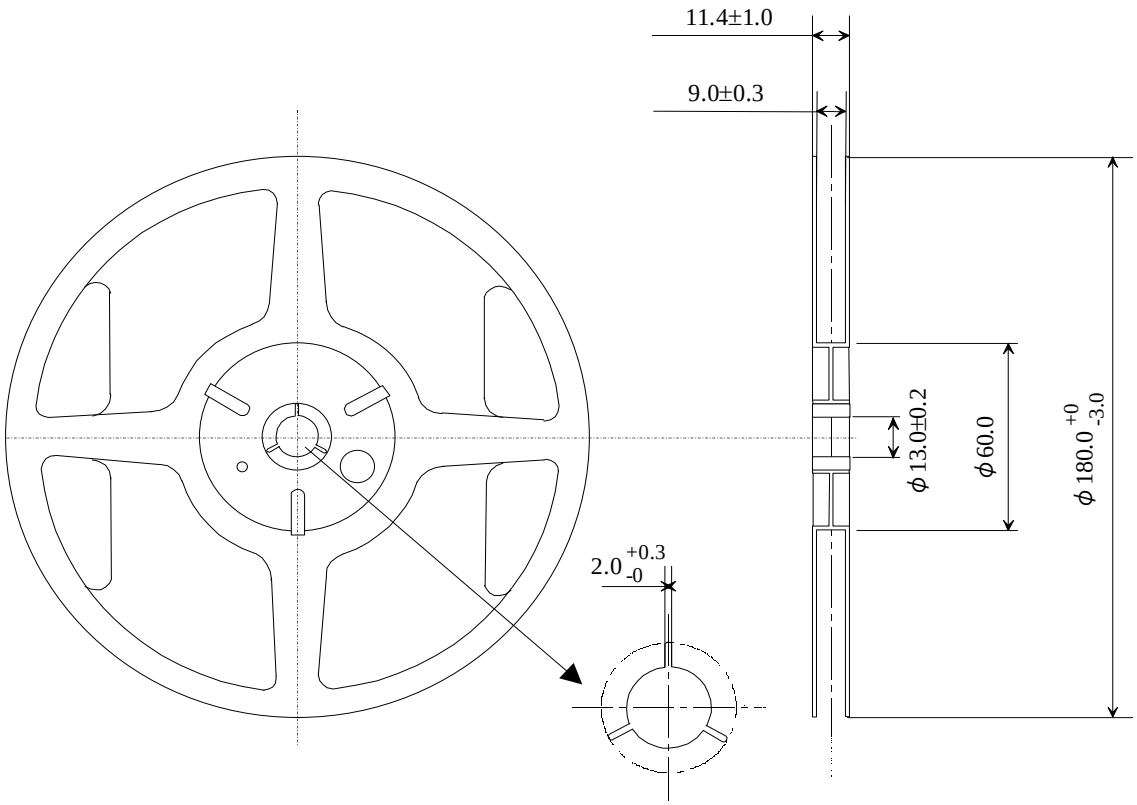
Material of the top tape : PET+PE



Unit : mm

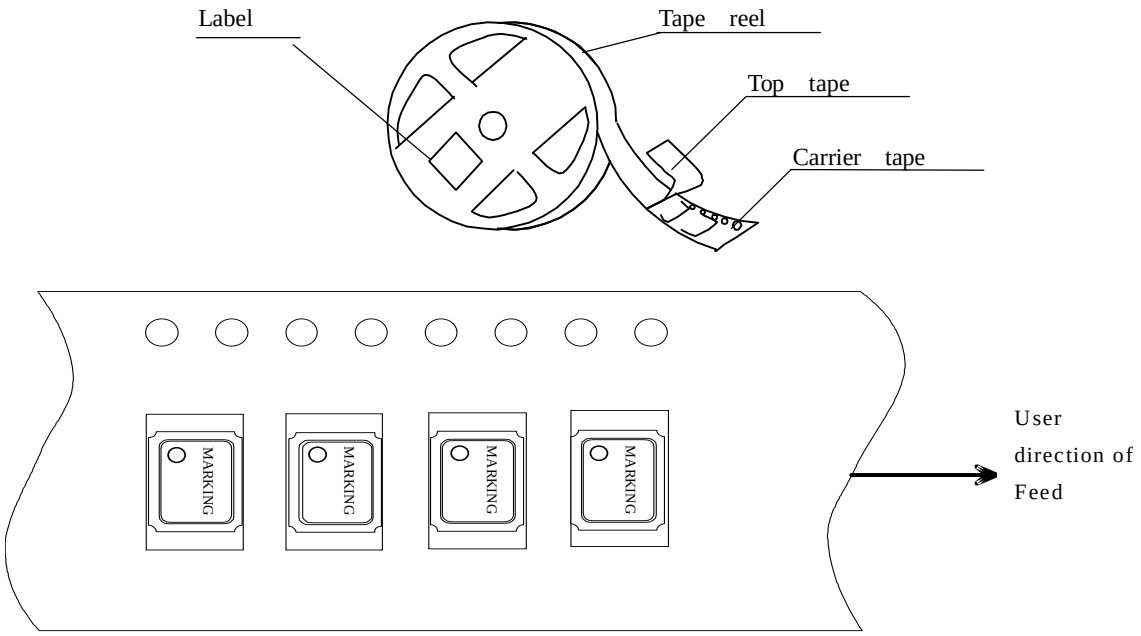
(2) Reel dimensions

Material of the reel : Conductive polystyrene

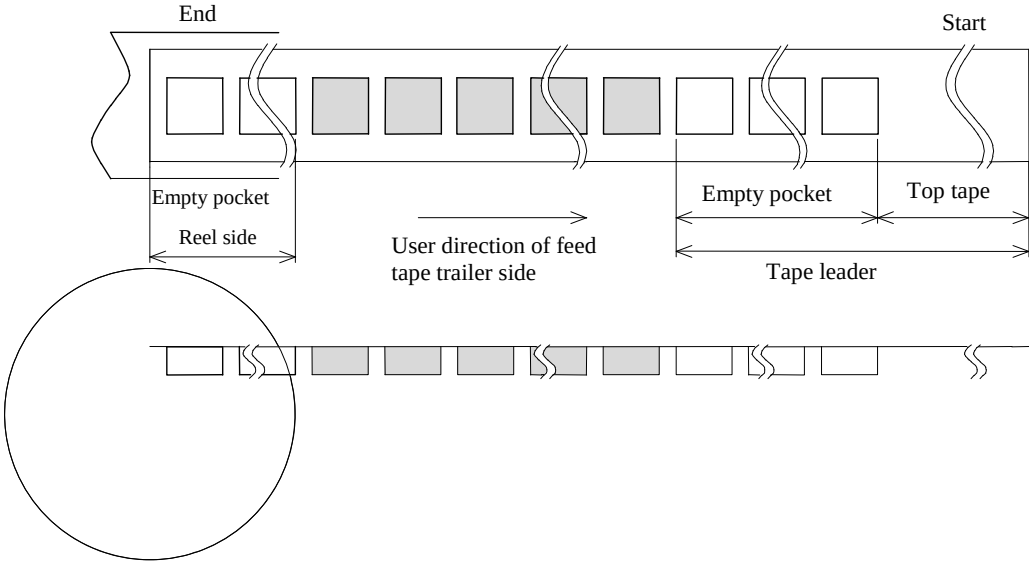


(3) Packing

①Tape & reel



②Start & end point



Item		Empty space
Tape leader	Top tape	Min. 1 000 mm
	Carrier tape	Min. 160 mm
Tape trailer	Top tape	Min. 0 mm
	Carrier tape	Min. 160 mm

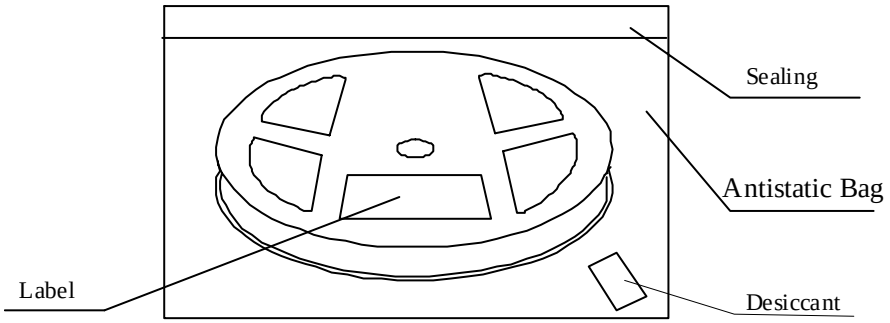
(4) Peel force of the cover tape

① angle : cover tape during peel off and the direction of unreeling shall be 165° to 180°.

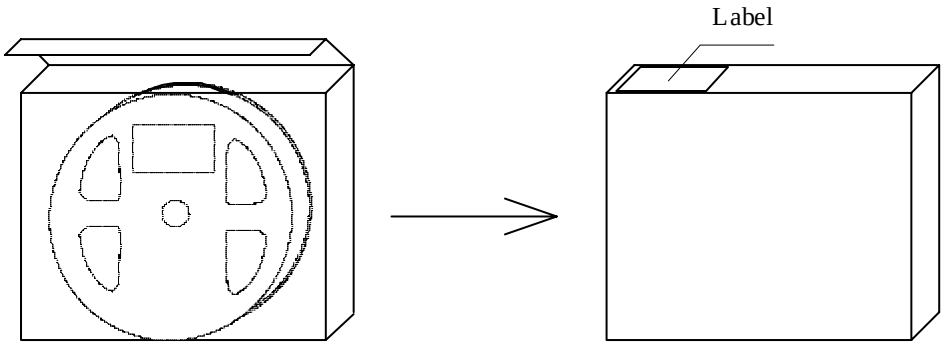
② peel speed : 300 mm / min.

〔2〕 Inner carton

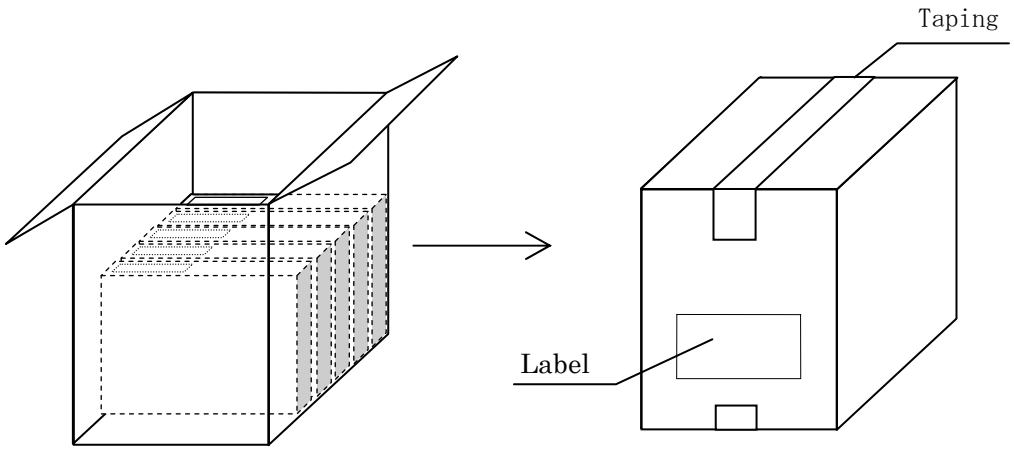
a) Packing to antistatic bag



b) Packing to inner carton



〔3〕 Shipping carton



〔4〕 Marking

(1) Reel marking

・Reel marking shall consist of :

1) Parts name
2) Quantity
3) Manufacturing date or symbol
4) Manufacturer's date or symbol
5) Others (if necessary)

(2) Inner carton marking

・Same as reel marking.

(3) Shipping carton marking

・Shipping carton marking shall consist of :

1) Parts name
2) Quantity

〔5〕 Quantity

・ 3 000 pcs./reel

〔6〕 Storage environment

(1) Before open the packing, we recommend to keep less than +30 °C and 85 %RH of Humidity, and to use it less than 6 months after delivery.
(2) We recommend to open Package in immediately before use. After open Package, We recommend to keeps less than 6 month. No need dry air before soldering work if it is less than temperature +30 °C, 85 humidity %RH.
(3) Not to expose the sun.
(4) Not to storage with some erosive chemicals.
(5) Nothing is allowed to put on the reel or carton to prevent mechanical damage.

〔7〕 Handling

・To handle with care to prevent the damage of tape, reel and products.

ATTN : HUAWEI TECHNOLOGIES CO., LTD.

No.QDS13-020
May. 8. 2013
SEIKO EPSON CORP.
TD·CS Quality Assurance Department

Parts Failure Rate

These parts failure-rate for SG-210STF are following.
Failure rate can be calculated with high temperature test result.
The calculation procedure of Failure rate is as follows.
This Series showed no failure during our with high temperature test result of +125 °C.

$$\text{Failure rate} \leq \frac{0.917}{4.4 \times 10^4 \times 578.72}$$

$$\leq 36 \text{ fit}$$

Total operating time ; 4.4×10^4
Acceleration factor ; 578.72
Reliability level ; 60 % (at +25 °C)