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HC-49U PRODUCT SPECIFICATIONS

***DATE:** 2005/2/17 ***FILE NUMBER:** P. 1/2

I. PART NUMBER: 9A04900070 **VER:** A

II. OPERATION CHARACTERISTICS

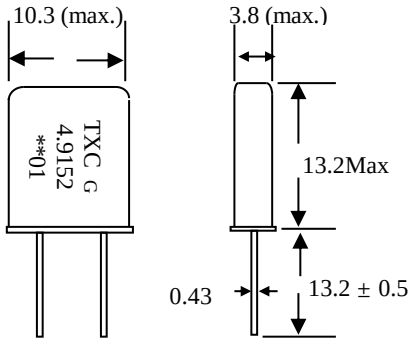
* 1. NORMINAL FREQUENCY: 4.915200 MHZ
* 2. MODE OF VIBRATION: ■ FUND □ 3rd □ 5TH
* 3. LOAD CAPACITANCE (CL): 20 pF
* 4. EFFECTIVE SERIES RESISTANCE: 50 Ω(MAX)
* 5. SHUNT CAPACITANCE (C0): 7 pF MAX. or pF to pF
* 6. CALIBRATION TOL. (25℃): ± 20 ppm
* 7. OPERATION TEMP. RANGE: -20 ℃～ + 70 ℃
* 8. STABILITY OVER OPERATION TEMP. RANGE: ± 30 ppm(MAX.)
* 9. DRIVE LEVEL: 1 uW(MAX.)
10. INSULATION RESISTANCE: 500MΩMin. at DC 100V.

III. OTHER:

11. UNWANTED MODES: N/A
12. LONG TERM STABILITY :± 3ppm / Year
*13. MOTIONAL CAPACITANCE(CM): N/A
*14.C0/CM: N/A
15.OTHERS:

***符合RoHS要求,且無鉛製程 (Pb <40ppm)**

IV. MARKING: UNIT: mm



V. PACKING METHOD :

1. PACKING: ■ TRAY (TXC STANDARD, White color) □ TRAY (Antistatic Material,___ Color)
□ TAPING ON REEL □ TAPING (AMMO PACKING)
□ ANTISTATIC BAG (_____Color), _____ Pcs/Bag.
□ ZIPPER PLASTIC BAG (____ Color),_____Pcs/Bag. □ OTHERS : _____

2. INNER BOX : ■ TXC STANDARD □ OTHERS _____

VI. RELIABILITY SPECIFICATIONS:(OTHERS REFER TO TXC QA SPEC.)

1. HERMETIC SEAL:LESS THAN 1×10^{-8} atm cc/sec.
2. MARKING PERMANENCY : Per MIL-STD-202,Method 215.
3. DROP TEST: Free dropping 3times, at 90cm height, drop on 3cm hard wood.
4. SOLDERABILITY: 95% coverage by using 63/37 solder at 245℃ for 5 sec. dipping.
after immersion in Alpha 611 flux for 5sec.

***REMARK: *** □ NO MARKING ■ MARKING
第一 " * " =>製造廠代碼
第二 " * " =>日期代碼

REVISION AND ENGINEERING CHANGE RECORDS

Ver.	Description of Change	Date	Approved	Check	Present
A	INITIAL RELEASED	2005/2/17			
B					
C			TON	SIMON	柯淑貞

INSPECT SPECIFICATIONS

依頻率訂定一般電氣規格要求如下: (註 : 如規格另有特殊規定，則依"外製成品49U"規格書)

1.) FDLD & DLD2規格 如下:

Frequency Range	FDLD製規	DLD2製規	FQC / IQC DLD2規格
Fundamental	(ppm) Max.	(Ohms) Max.	(Ohms) Max.
4MHz(不含)以下	10	15	15 Ω (10+10*50%)
4MHz(含) ~ 8MHZ(含)	10	15	15 Ω (20+20*50%)
8MHz(不含)~18MHz(含)	10	10	15 Ω (10+10*50%)
18MHz(不含)~以上	15	12	15 Ω (10+10*50%)
3rd Overtone			
24MHz~100MHz	10	20	30 Ω (20+20*50%)
			(FDLD 允收: 15ppm max.)

2.) DLD POWER 至少設定 0.1、1、 10、 50、 100 uW

3.) Spurious規格修訂如下:

Frequency Range	Spurious spec.
Fundamental	Search Range(-5000~+5000ppm)
20MHz(含)以下	-5.0 dB Max.
20MHz(含)以上	-3.0 dB Max.
3rd Overtone	
24MHz~60MHz	-3.5 dB Max.
60MHz~100MHz	-3.0 dB Max.

4.) TXC IQC 抽樣檢驗DLD2依MIL-STD-105E Level Ⅱ& Single sampling plans ：

a.)2003/4/24~6/30 依照AQL 0.4允收。

b.)2003/7/1 起依照AQL 0.1允收。

Verion	A	Revised		Prepared by	Shu-Chen Ko
Date	2003/4/24	DCC. No.	製品管理辦法A.1	Check by	Simon Wang
Issued by	M.E.Department	Hold Type	HC-49/S	Approved by	Joseph